

REMARKS

Claims 1, 3-7, 9-10 and 14-16 are currently pending in the present application, of which Claims 7 and 10 have been amended.

The acronym "FET" in Claims 7 and 10 has been amended as "field effect transistor." Thus, the claim objection is believed to be overcome.

Rejection under 35 U.S.C. § 102

Claims 1, 3-6 and 9 were rejected under 35 U.S.C. § 102(e) as being anticipated by *Wecker et al.* (US 6,289,464). Applicants respectfully traverse such rejection.

Claim 1 recites "providing a plurality of status bits to indicate whether or not a RF module is attached to said computer and is activated" (lines 3-4). On page 3 of the Final Office Action, the Examiner asserts that the claimed providing step is disclosed by *Wecker* because interface controller 66 of *Wecker* is "designed and operates according to known industry standards such as PCMCIA and Compact Flash Specifications."

The Examiner is basically asserting that the claimed status bits must exist in *Wecker* because there are communications between RF receiver 60 and interface controller 66. But the fact is that the claimed status bits are not explicitly taught or suggested by *Wecker*. The reason *Wecker* does not require the claimed status bits is because in *Wecker*, RF receiver 60 is always attached to a computer. As such, *Wecker* does not have to concern with RF receiver 60 not being attached to a computer. Such is evidenced by the fact that the claimed step of "determining whether said RF module is attached to said computer and is activated by reading said plurality of status bits" (lines 8-9) is not found in *Wecker*. Because the claimed invention includes novel features that are not taught or suggested by *Wecker*, the § 102 rejection is believed to be overcome.

Rejection under 35 U.S.C. § 103

Claims 10 and 14-16 were rejected under 35 U.S.C. § 103(a) as being unpatentable over *Wecker et al.* (US 6,289,464) in view of well-know prior art in the field of the invention. Applicants respectfully traverse such rejection.

In addition to "a plurality of status bits for indicating whether or not a RF module is attached to said computer and is activated (lines 6-7), Claim 10 also recites "a field effect transistor (FET) switch for enabling power to said receiving means when said computer is in said power-saving mode" (lines 11-12). On page 6 of the Final Office Action, the Examiner states that *Wecker* does not "provide any details regarding the types of electronic switch that is used to provide power to the device," but the Examiner then asserts that "it would have been obvious to one skilled in the art at the time of the invention to use an FET switch to maintain power to said receiving means."

According to *Wecker*, "the wireless receiver 27 is powered and capable of receiving information from the wireless transport 14 at all times irrespective of whether other components of the mobile device 10 are operational and receiving power" (emphasis added). Since *Wecker*'s wireless receiver 27 is powered and capable of receiving wireless information at all times, there would be no reason to use a switch, whether a FET switch or other types of switch, because the point of using a switch to control the supply of power to the receiving means. When wireless receiver 27 is powered at all times, a switch would be perfunctory. Thus, it is clear that *Wecker* does not teach or suggest a switch, and definitely not the claimed FET switch. Because the claimed invention includes novel features that are not taught or suggested by *Wecker*, the § 103 rejection is believed to be overcome.

CONCLUSION

Claims 1, 3-7, 9-10 and 14-16 are currently pending in the present application.

For the reasons stated above, Applicants believe that independent Claims 1 and 10 along with their respective dependent claims are in condition for allowance. The remaining prior art cited by the Examiner but not relied upon has been reviewed and is not believed to show or suggest the claimed invention.

No fee or extension of time is believed to be necessary; however, in the event that any fee or extension of time is required for the prosecution of this application, please charge it against Deposit Account No. **50-0563**.

Respectfully submitted,



Antony P. Ng
Registration No. 43,427
BRACEWELL & PATTERSON, LLP
111 Congress Ave., Suite 2300
Austin, Texas 78701
(512) 343-6116

ATTORNEY FOR APPLICANTS